

POWER SAVING METHOD AND ARRANGEMENT FOR A RECONFIGURABLE ARRAY

ABSTRACT OF THE INVENTION

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A method and arrangement for reducing power consumption in an M row x N column array of processing cells. A row mask register masks individual cells in each row for being enabled. A column mask register masks individual cells in each column for being enabled. The combination of the row mask register signal and column mask register signal enables or disables each cell of the array. Enabled cells are activated to execute an operation or function, while disabled cells are prevented from consuming dynamic power. Depending on the application and enabled cells thereby, power consumption is reduced.

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